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Docket: 0756-1139

78-102
22-103

1104

7/C
3-9-96
M. Ross

1/3/96
J. A. Kins

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of:)
Shunpei YAMAZAKI et al.)
Serial No. 08/355,652) Examiner Chaudhari
Filed: December 14, 1994) Art Unit: 1104
For: LASER PROCESSING METHOD,)
METHOD FOR FORMING A)
FLASH MEMORY, INSULATED)
GATE SEMICONDUCTOR DEVICE)
AND METHOD FOR FORMING)
THE SAME) February 7, 1996

AMENDMENT

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GROUP 1100

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

In response to the Office Action mailed August 8, 1995, please amend the above-identified application as follows.

IN THE CLAIMS:

Please amend claims 21, 26, 31, 36, 41, 45, 36 and 49 as follows:

21. (Amended) A laser processing method comprising:
forming a gate member on a semiconductor;

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